

Anti-Alias Filter and FFT Spectrum Analyzer Modules



CR9000X



CR9052DC



CR9052IEPE

Overview

Campbell Scientific's CR9052IEPE and CR9052DC modules provide a high-performance anti-alias filter and a Fast Fourier Transform (FFT) spectrum analyzer that extend the capabilities of the CR9000(X) Measurement and Control System. Use of these CR9052 modules allow the CR9000(X) to perform digitally filtered, alias free simultaneous measurements.

Triaxial accelerometers, microphones, and strain gages all benefit from the CR9052's superior noise performance. With the CR9052 in the CR9000(X), the user has a rugged, portable, and accurate

dc-powered system that rivals the performance of larger laboratory-based, ac-powered data acquisition systems. Each CR9052 occupies one slot in a CR9000(X) system chassis.

Both the CR9052IEPE and CR9052DC modules have six analog differential channels with programmable input ranges. Each channel has its own programmable-gain instrumentation amplifier, pre-sampling analog filter, and sigma-delta analog-to-digital converter. All of the channels in a single CR9000(X) chassis are sampled simultaneously.

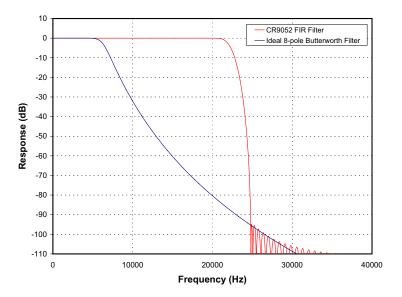
Anti-Aliasing Filter

The CR9052 implements anti-aliasing with programmable, realtime, finite impulse response (FIR) filters. An on-board digital signal processor (DSP) collects alias-free, 50 kHz samples from each of the module's sigma-delta converters, and then applies real-time, programmable low-pass filtering and decimation to anti-alias and down-sample the data to the desired measurement rate. The CR9052 can also accumulate snapshots of anti-aliased time-series, Fourier transform them into frequency spectra, and send the resulting real-time spectra to the CR9000(X)'s main processor.

The CR9052 can burst measurements to its on-board, eight million sample buffer at 50,000 measurements per second for each channel. Using the FFT spectrum analyzer mode, the module's DSP can provide real-time spectra from seamless, anti-aliased, 50 kHz, 2048-point time-series snapshots for each of its six analog input channels. The decimated data can be downloaded to an appropriate PC card at an aggregate rate of up to 300,000 measurements per second.



The CR9052 filter's pass-band ripple is less than ± 0.01 dB (0.1%), and the stop-band attenuation exceeds 90 dB (1/32,000). The FIR filter's transition band has a steep roll-off (graph below), with the stop-band frequency starting a factor of 1.24 above the pass-band frequency. In comparison, the stop-band frequency of an ideal eight-pole Butter-worth filter with the same ripple and attenuation starts a factor of 5.81 above its pass-band frequency.



The digital implementation of the CR9052 FIR filters maintains a group delay that is independent of frequency (linear phase response). In addition, the digital filter performance does not change with time, temperature, or component tolerances. The on-board DSP automatically chooses the appropriate low-pass filter to anti-alias the input data for the user's desired measurement rate. If desired, users may load their own coefficients into the on-board DSP to tailor the FIR filter's frequency response to their own needs (band pass, band reject, etc.).

The FFT option allows radix-two (2^n , where n = 5, 6, ... 16) transform lengths ranging from 32 to 65,536 samples. Users can

optionally apply a Hanning, Hamming, Blackman-Harris, Kaiser-Bessel window function to their time series before transforming them. The CR9052 offers a variety of spectrum normalizations, including real and imaginary, amplitude and phase, power, power-spectral density (PSD), and decibels (dB). In addition, the CR9052 can combine adjacent spectral bins into a single bin to decrease the size of the final spectrum. A built-in function selects an exponentially increasing spectral bin width to give 1/n octave analyses, where n can vary from 1 to 12. A single programming step with either the CRBasic programming language, or the CR9000(X) program generator configures the FFT spectrum analyzer options. Class A, B, and C spectral weighing options are supported with the CR9052.

The module has superior noise performance, with an input-referred noise of 8 nV Hz^{-1/2} for the ± 20 mV input range. On the ± 20 mV input range, the total noise for a 20 kHz bandwidth is less than 1.4 μ V, and for a 1 Hz bandwidth, 250 nV (see noise performance in specification table). The programmable anti-alias filter allows users to trade bandwidth for noise, or vice versa. The DSP's floating-point numeric implementation of the FIR anti-alias filters and Fourier transforms preserve this low-noise performance.

A 2048-point FFT gives an instantaneous dynamic range exceeding 126 dB (an amplitude ratio of 2×10^6), and the 65,536-point FFT gives an instantaneous dynamic range exceeding 140 dB (an amplitude ratio of 1×10^7). Real-time digital temperature compensation ensures gain accuracy ($\pm0.03\%$ of reading) and offset accuracy ($\pm0.03\%$ of full-scale) throughout the -40° to 70°C operating temperature range.

The combined capabilities of the CR9052 and the CR9000(X) offer numerous measurement and data processing possibilities. For example, this combination allows users to mix high-speed, antialiased measurements with slower measurements. The CRBasic program of the CR9000(X) allows users to process their data before saving it to data tables. For example, users may save measured data only if the amplitude of a specific acoustic frequency exceeds some threshold, or only if an acoustic spectral component correlates to measurements from other sensors.

CR9052IEPE versus CR9052DC

The CR9052IEPE and CR9052DC use the same anti-alias motherboard but have different daughterboards that allow measurement of different sensor types. The CR9052IEPE's daughterboard provides current source excitation and ac coupling that allow the direct connection of Internal Electronics Piezoelectric (IEPE) accelerometers and microphones. An external IEPE power supply is not needed because the CR9052IEPE has an on-board 34 V, high efficiency, low noise, charge pump power supply that supplies

a 2 mA, 4 mA, or 6 mA constant current excitation. The CR9052IEPE interfaces with sensors via BNC connectors



The CR9052DC module includes six independent dc excitation channels that are programmable for 10 V, 5 V, or 10 mA. This module measures accelerometers, strain gages, and microphones.

Sensor connections use CR9052EC Easy Connectors for the CR9052DC. The easy connectors consist of a terminal strip that

is easily disconnected from the CR9052. Customers needing to monitor several locations intermittently have found it useful to buy several CR9052ECs and simply move the CR9000(X) and on-board CR9052DC(s) between monitoring locations.



Prior to April 2005, only the CR9052DC was available. Users with a CR9052DC may send it to Campbell Scientific to convert it to a CR9052IEPE. The conversion consists of adding the IEPE daughterboard and a recalibration. Once converted, the customer can swap back and forth between the DC and IEPE excitation boards.

Specifications

Operating temperature range is -40° to +70°C (specifications valid over this range unless otherwise specified). Non-condensing environment is required. To maintain specifications, Campbell Scientific recommends recalibrating dataloggers every two years. If more than four CR9052s are to be used in a single chassis, consult with a Campbell Scientific applications engineer for application-specific requirements. We recommend that you confirm system configuration and critical specifications with Campbell Scientific before purchase.

OVER-VOLTAGE PROTECTION ON ALL INPUTS & OUTPUTS: + 50 V, -40 V

CURRENT CONSUMPTION (AT 12 V INPUT): 500 mA + 1.5*[I_{ex}], where I_{ex} is the sum of excitation currents provided by all channels

CURRENT CONSUMPTION FOR COMPLETE CR9000(X) SYSTEM: must be less than 4 A

DIFFERENTIAL INPUTS

NUMBER OF CHANNELS: 6

GAIN ACCURACY: ±0.03% of reading

OFFSET ACCURACY: ±0.03% of full-scale input range

INPUT RESISTANCE: $1 \times 10^9 \Omega$

INPUT TIME CONSTANT: $1 \text{ k}\Omega \times 100 \text{ pF} = 100 \text{ ns}$

INPUT OFFSET CURRENT: ≤ 35 nA

COMMON-MODE INPUT RANGE: +15 to -5 V

PROGRAMMABLE ANTI-ALIASING IMPLEMENTED WITH FINITE-IMPULSE-RESPONSE FILTERS

 f_{SAMPLE} = Output sample rate that is programmable from 50 ksamples s⁻¹ to 5 samples s⁻¹

 $f_{\text{SAMPLE}}/f_{\text{PASS}} = \text{Sample ratio that is programmable (2.5, 5, 10, or 20)}$

 f_{PASS} = Top of the pass band

 $f_{\text{STOP}} = \text{Bottom of the stop band}$

 $f_{\text{PASS}}/f_{\text{STOP}}$ = Transition band roll off

Sample Ratio	$f_{\scriptscriptstyle{PASS}}$	$f_{ extsf{STOP}}$	$f_{\scriptscriptstyle \sf PASS}$ / $f_{\scriptscriptstyle \sf STOP}$
2.5	$f_{ m SAMPLE}$ / 2.5	$f_{ m SAMPLE}$ / 2.01	1.24
_		$f_{ m SAMPLE}$ / 3.37	1.48
		$f_{ m SAMPLE}$ $/$ 5.08	1.97
20		$f_{ m SAMPLE}$ $/$ 6.81	2.94

LINEAR PHASE RESPONSE: group delay is independent of frequency

PASS BAND RIPPLE: ≤0.01 dB

STOP BAND ATTENUATION: ≥90 dB

GROUP DELAY: $36/f_{SAMPLE}$

CHANNEL-TO-CHANNEL SAMPLING SIMULTANEITY: ≤ 100 ns

MEASUREMENT RATES

NON-BURST: 15 ksamples s⁻¹, aggregate*

BURSTING TO PC FLASH CARD: 50 ksamples s⁻¹, aggregate*

BURSTING TO ROTATING MEDIA PC CARD: 100 ksamples s⁻¹, aggregate*

BURSTING TO 8 M SAMPLE BUFFER ON FILTER MODULE: 300 ksamples s⁻¹, aggregate per module**

*The aggregate rate is the sum of the measurement rates on all channels **The aggregate per module rate is the sum of measurement rates on all channels of a single filter module.

Full-Scale Diff. Range	Noise Performance	Dynamic Range (f _{PASS} = 10 Hz	CMRR •
±5000 mV	50 μV + (600 nV * $(f_{PASS})^{-1/2}$)	106 dB	-70 dB
±1000 mV	10 μ V + (150 nV * (f_{PASS}) $^{-1/2}$)	106 dB	-70 dB
±200 mV	$2 \mu V + (30 \text{ nV} * (f_{PASS})^{-1/2})$	106 dB	-83 dB
±50 mV	0.5 μ V + (12 nV * $(f_{PASS})^{-1/2}$)	106 dB	-95 dB
±50 mV	0.25 μ V + (8 nV * (f_{PASS}) $^{-1/2}$)	103 dB	-103 dB

•CMRR = common-mode rejection ratio specified from dc to 500 Hz = (common-mode gain)/(differential-mode gain)

FFT SPECTRUM ANALYZER

Fourier transforms applied to anti-aliased inputs described above

NUMBER OF CHANNELS: 6

TIME SERIES SAMPLE RATES: programmable from 50 ksamples s⁻¹ to 5 samples s⁻¹

FFT LENGTH: programmable from 32 to 65,536 samples

REAL-TIME SPECTRAL THROUGHPUT FOR SIX CHANNELS: 50 kHz or slower, 2048-point or smaller, seamless snapshots

REAL-TIME SPECTRAL THROUGHPUT FOR TWO CHANNELS: 50 kHz or slower, 65536-point or smaller, seamless snapshots

OPTIONAL TIME SERIES WINDOWS: Hanning, Hamming, Blackman

SPECTRUM OPTIONS: Real and imaginary, Amplitude and phase, Amplitude, Amplitude rms, Power, Power spectral density, dB

OPTIONAL SPECTRAL BINNING TO REDUCE FINAL SPECTRUM LENGTH

LINEAR SPECTRAL BINNING: $2 \le m \le (FFT_length / 2)$, where programmable m adjacent bins are combined into a single bin

LOGARITHMIC SPECTRAL BINNING: $1 \le n \le 12$, where exponentially increasing spectral bin width gives 1/n Octave Analyses

Specifications (continued)

CR9052DC EXCITATIONS

NUMBER OF CONTINUOUS EXCITATION CHANNELS: 6

Programmable Excitation Levels	Compliance	Accuracy	
10 V	85 mA	±0.03% of setting (-25° to 50°C) ±0.05% of setting (-40° to 70°C)	
5 V	85 mA	±0.03% of setting (-25° to 50°C) ±0.05% of setting (-40° to 70°C)	
10 mA	12 V	±0.06% of setting (-25° to 50°C) ±0.08% of setting (-40° to 70°C)	

DC EXCITATION NOISE SUMMARY

Input Range at 25°C (mV)	DC Excitation Noise Floor (nV Hz -½)	
±5000	791	
±1000	190	

CR9052IEPE EXCITATIONS

NUMBER OF CONTINUOUS EXCITATION CHANNELS: 6

CHANNEL INDICATORS: one open circuit indicator and one short circuit indicator per channel. Short circuit indicator also indicates when a channel is over-driven or under-driven.

CHANNEL CONNECTOR TYPE: BNC

CHANNEL INDICATORS: one open circuit indicator and one short circuit indicator per channel. Short circuit indicator also indicates when a channel is over-driven or under-driven.

BUILT-IN CONSTANT CURRENT SOURCE: each channel's current source is independently software programmable to 0 mA, 1.9 mA, 3.7 mA, or 5.6 mA

CURRENT SOURCE COMPLIANCE RANGE: 0 to 30 Vdc

SIGNAL FREQUENCY RANGE: programmable 0.03 Hz to 20 kHz, or 3 Hz to 20 kHz

AC ACCURACY: ±0.05% over -40° to +70°C

ESD PROTECTION: each channel spark-gap protected

IEPE CONDITIONING AND NOISE SUMMARY

The noise floor is computed from $1 \times 10^9 (\overline{PSD(f)})^{-1/2}$; where PSD(f) is the power spectral density function in V² Hz⁻¹, and 100 Hz $\leq f \leq$ 20 kHz. The input is shorted through a 4.42 Ω resistor.

Input Range (mV)	IEPE with 5.6 mA	IEPE with 3.7 mA	IEPE with 1.9 mA
	Noise Floor	Noise Floor	Noise Floor
	(nV Hz ^{-½})	(nV Hz ^{-½})	(nV Hz ^{-½})
±5000	1060 at 25℃	980 at 25℃	960 at 25°C
	1130 at 70℃	1060 at 70℃	970 at 70°C
±1000	600 at 25°C	490 at 25°C	420 at 25°C
	700 at 70°C	580 at 70°C	490 at 70°C